

PATENT NUMBER

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U.S. **UTILITY** Patent Application

O.I.P.E. M.H. <u>PD</u> SCANNED <u>PD</u> Q.A. <u>MT</u>	PATENT DATE
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APPLICATION NO. 09/733781	CONT/PRIOR F	CLASS 257	SUBCLASS 539	ART UNIT 2811 7815	EXAMINER Warren
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NTS
David M. Patti

High-voltage integrated vertical resistor and manufacturing process thereof

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				NOTICE OF ALLOWANCE MAILED	
				ISSUE FEE	
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<input type="checkbox"/> The terminal ____months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
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